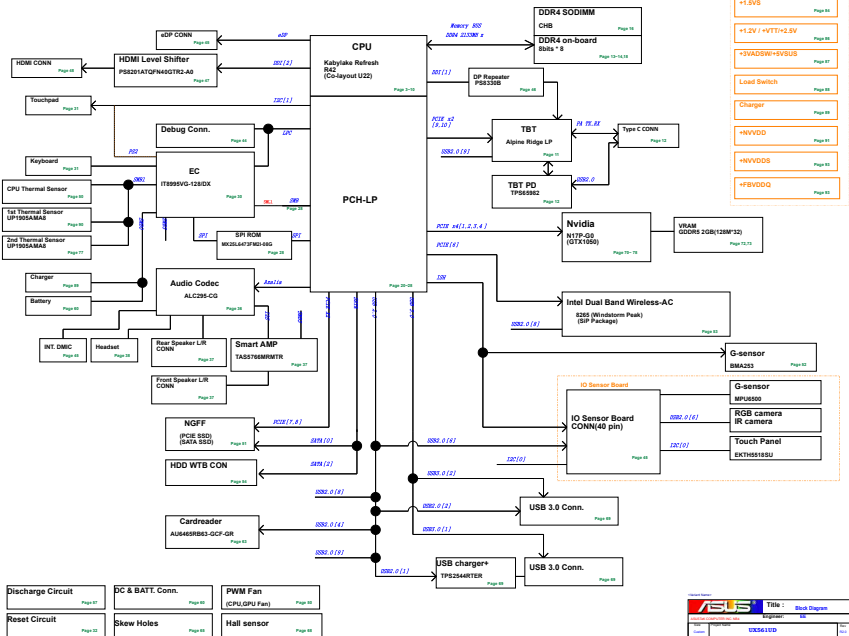


UX561UD SCHEMATIC Revision 2.0

BLOCK DIAGRAM

SYSTEM PAGE REF.

PAGE	Content
1	Block Diagram
2	System Setting
3	CPU_DISPLAY
4	CPU_CG04
5	CPU_LFQ, SPT, SW, CLINE
6	CPU_POWER
7	CPU_XSP
8	CPU_MISC, JTAG
9	CPU_CG0, SVD0
10	CPU_POWER_CAP
11	TBT_Alpine-Ridge
12	TBT_T2663824Type C
13	CG04_TRANSMISSION
14	CG04_ON-BOARD_A
15	DDR4_SO-DIMM_B
16	DDR4_CA_VOLTAJE
17	CPU_PCH_CG12, HMC
18	CPU_PCH_CG10, LFQ10, MISC
19	CPU_PCH_AUDIO, SDD0, SDDC
20	CPU_PCH_PCIE, USB, DATA
21	CPU_PCH_CLOCK SIGNALS, RTC
22	CPU_PCH_SVS_POWER
23	CPU_PCH_POWER, GND
24	CPU_PCH_POWER, GND
25	PC0-SPT ROM, CG0
26	KMC_128935
27	EC_KA_TP_TPM
28	RST_Reset Circuit
29	AD0-ALC294+
30	AUD-HEADPHONE & SPEK
31	MIC-IN, SW, TAS2766K
32	BUG_Debug
33	CRT_LCD_Panel_CG0C_DMC
34	HDMI_Level_Shift
35	HDMI Conn
36	FAN_Thermal Sensor
37	NGTF SSD
38	G-sensor
39	WLAN(S1P)
40	HDD CON
41	LED_Indicator
42	DP_Discharge
43	PRO_PROTECT
44	PW_DC JACK / BAT CON
45	ME_Conn & Skew Hole
46	B to B CON
47	USB3 CON
48	VGA_PCI-EXPRESS (1)++
49	GPU_FB-IF_CG0C (2)++
50	FRAME_BUFFER-A (3)++
51	FRAME_BUFFER-B (4)++
52	VGA_CRT/VG05 (5)++
53	VGA_GPI0/DVI/DP (6)++
54	VGA_XTAL/STAPPING (7)++
55	VGA_PMG/GND (8)++
56	VSWM CAP
57	080_PW_KABY LAKE-U(1)
58	081_PW_KABY LAKE-U(2)
59	082_PW_+1.8VSBUS (+1.8VSBUS
60	083_PW_+1.5VS
61	084_PW_+1.5V
62	085_PW_+1.2V/+VTTI+2.5V
63	086_PW_+SVADSW+SVBUS
64	087_PW_LOAD SWITCH
65	088_PW_CHARGER
66	089_PW_PROTECTION
67	091_PW_+NVDD (RT8191 2Phase)
68	092_PW_+NVDD0 (RT8191A 1Phase)
69	093_PW_+NVDD0 (RT8191A 1Phase)
70	094_PW_+FBVDDQ (RT8191A 1Phase)
71	100_AC Power On Timing
72	101_POWER-ON SEQUENCE
73	102_History

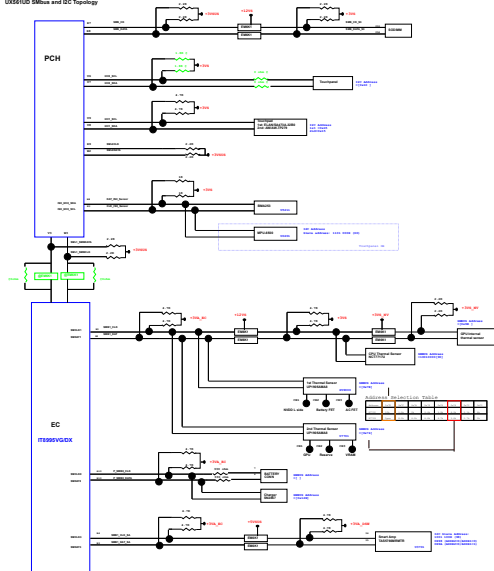


Power

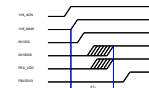
VCC0C0E1	VCC0TIFVCCSA	Page 82
+1.8VSBUS	+1.8VSBUS	Page 83
+1.5VS		Page 84
+1.2V / +VTTI+2.5V		Page 85
+SVADSW+SVBUS		Page 87
Load Switch		Page 88
Charger		Page 89
+NVDD		Page 90
+NVDD0		Page 91
+FBVDDQ		Page 92

Revision 2.0

ASUS	Title : Block Diagram
Author: UX561UD-UX561UD	Signature: 08
Version: 2.0	File: UX561UD
Created: 2018-01-11	Rev: 1.0



GPU Power Up Power Rail Sequence



GPU Power Down Sequence

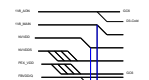
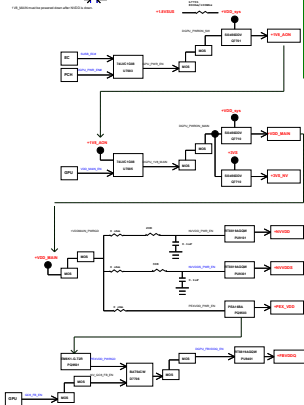
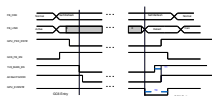


Fig. 10.6.6. Multiple preserved lines after K2CO₃ is done.

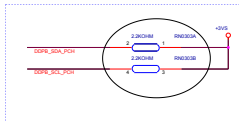
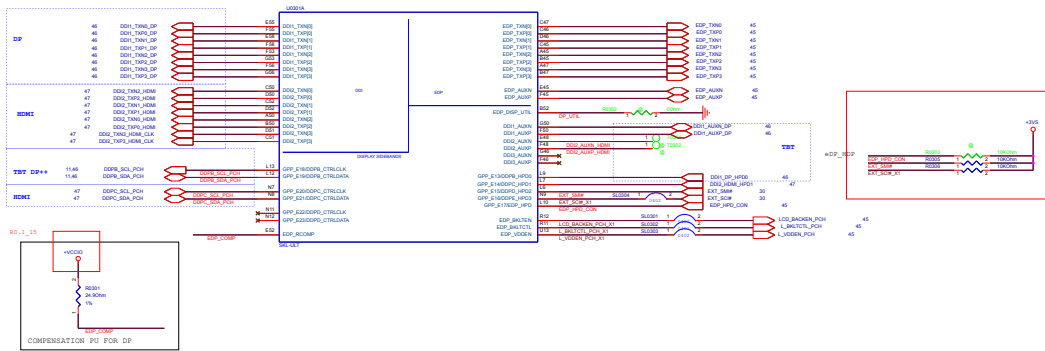


GC6.2.1 Entry/Exit Timing

[illegible]

Display Port

A	EDP
B	DP
C	HDMI

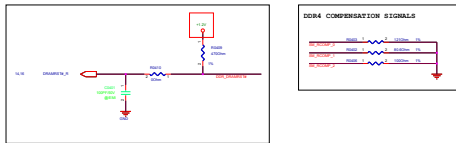
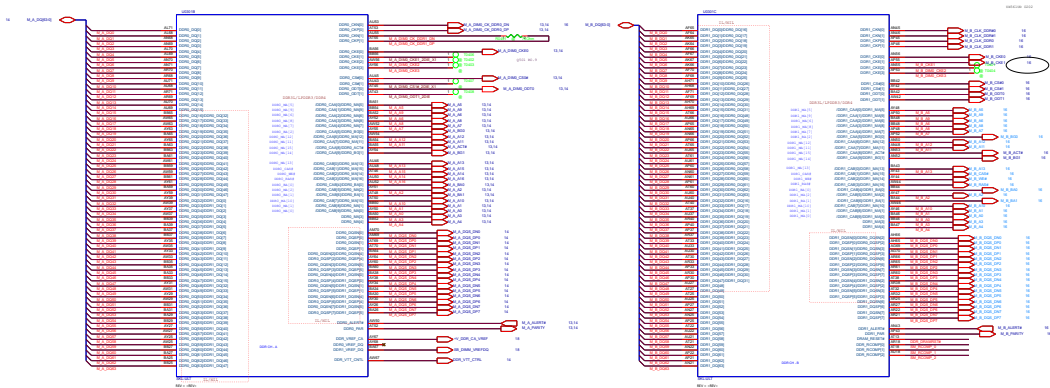


```
DDP Strap Setting Update:
0 = Port is not detected (Default)
1 = Port is detected
```

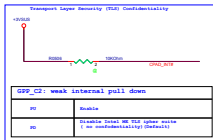
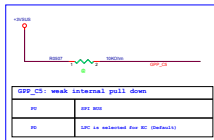
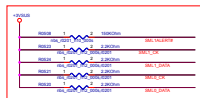
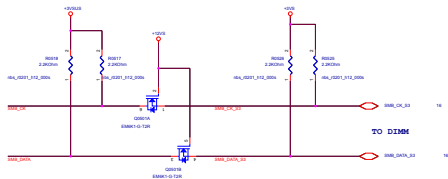
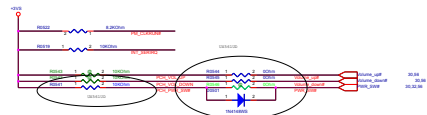
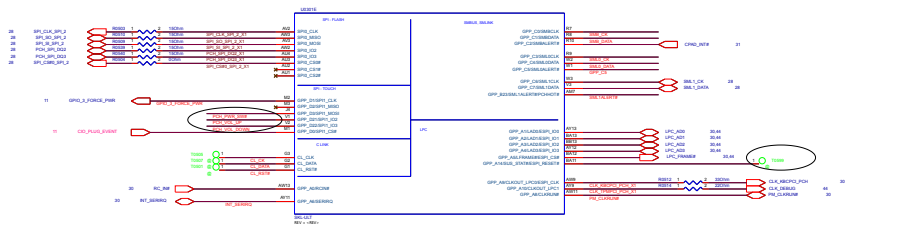
R1.1 9

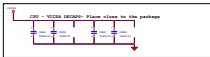
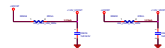
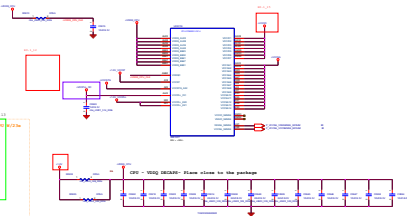
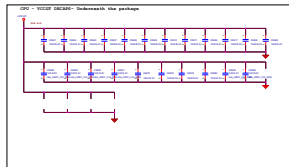
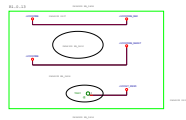
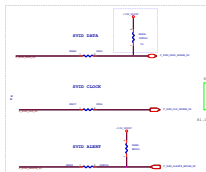
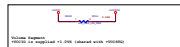
Variant Names

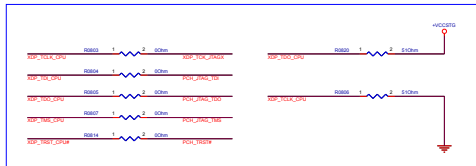
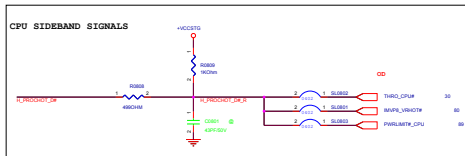
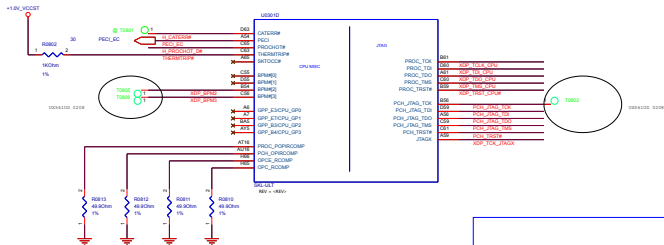
		Title : CPU_DISPLAY	
ASUSTek COMPUTER INC. INDIA		Engineer: EE	
Size Custom	Project Name		Rev R2.0
Date: Sat, 26 Jun 2021	Sheet: 5	of: 5	100



© 2008 ASUS

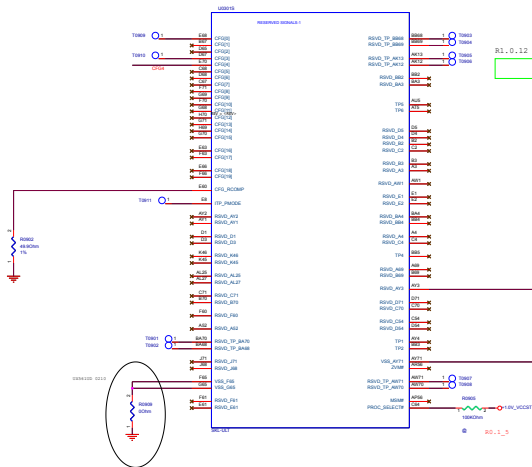




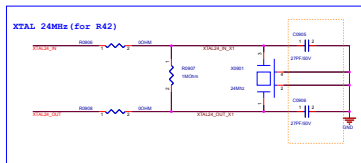


BOM

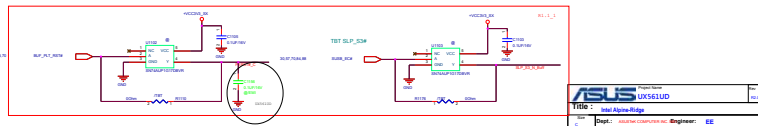
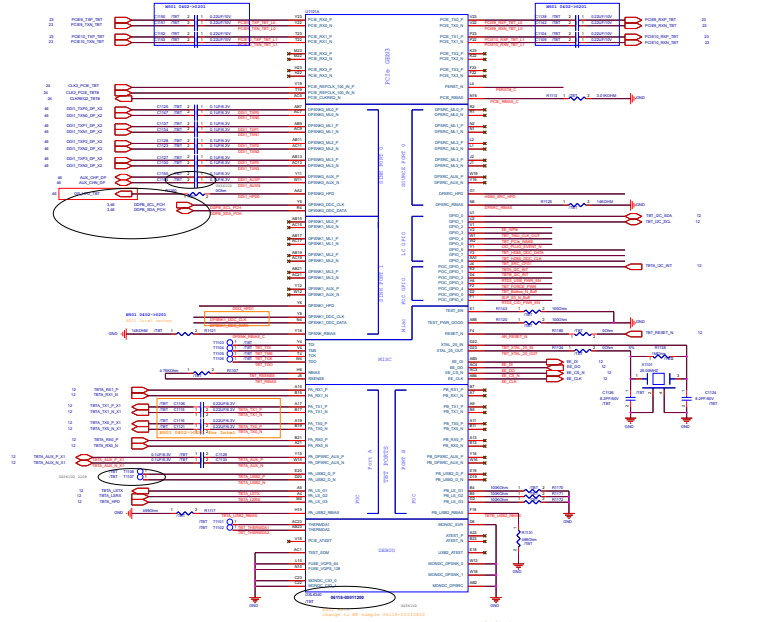
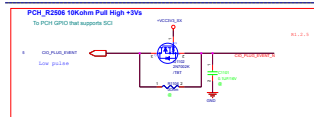
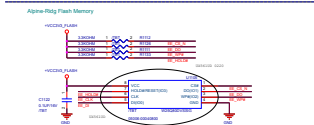
ASUS		Title : CPU_MISC,JTAG,CLK	
ASUSTeK COMPUTER INC. N/A		Engineer: EE	
Size	Project Name	Rev	
8	UX561UD	R2.0	
Date	Friday, June 01, 2017	Sheet	8 of 107

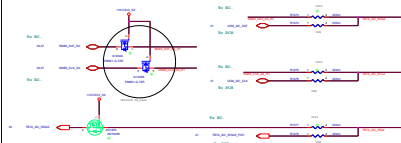
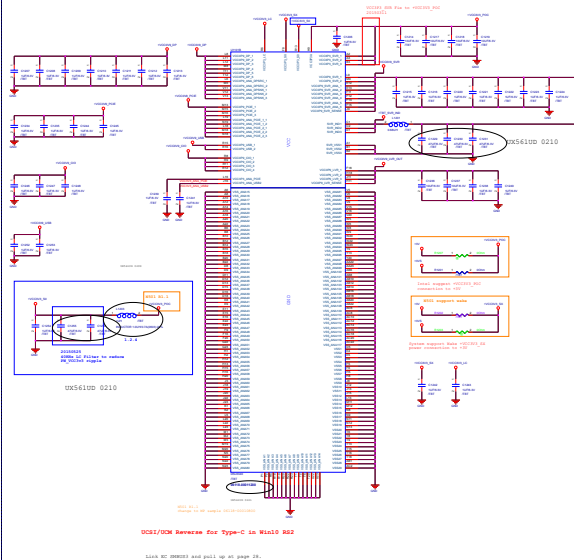
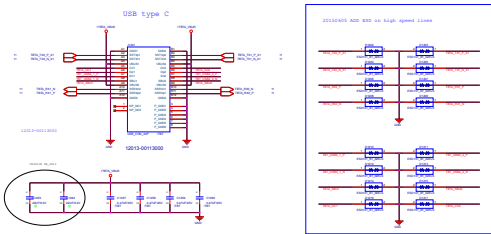
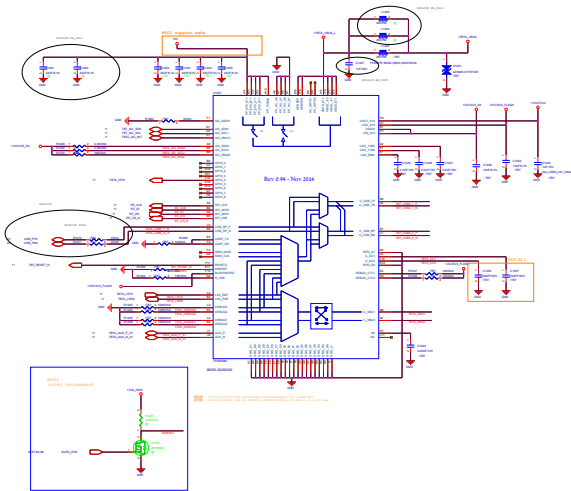


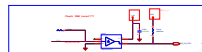
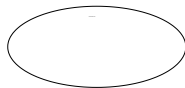
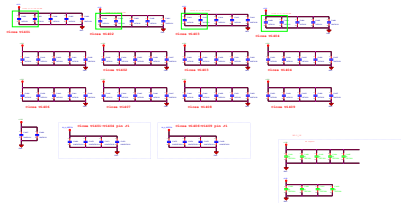
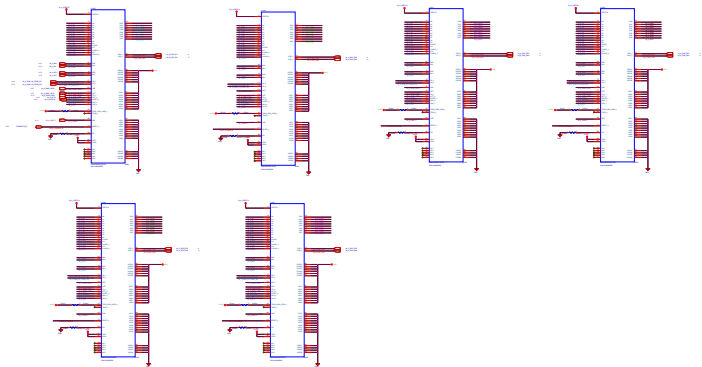
2014/11/24 Intel review
U11/U12 need to be connected to lxluF
decoupling cap placeholder for future platform
compatibility purpose.



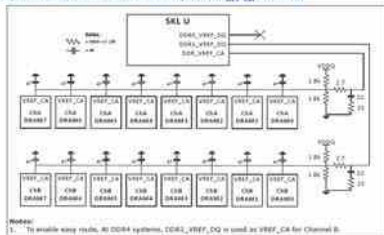
	1	0	NOTE
CFG4	DISABLE	ENABLE	40P ENABLE



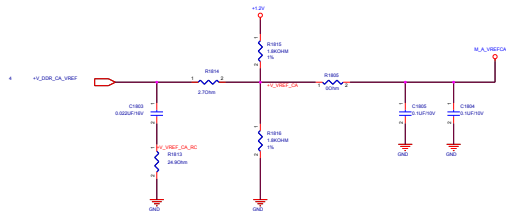
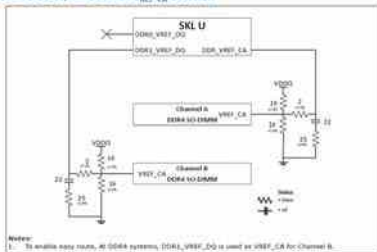




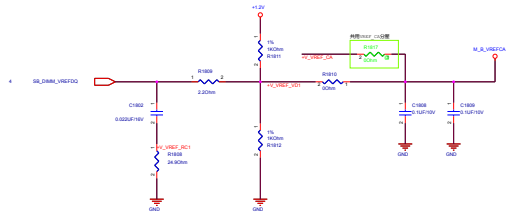
SODIMM CHB-DIMM0
TOP H4.0mm STD (J1601)

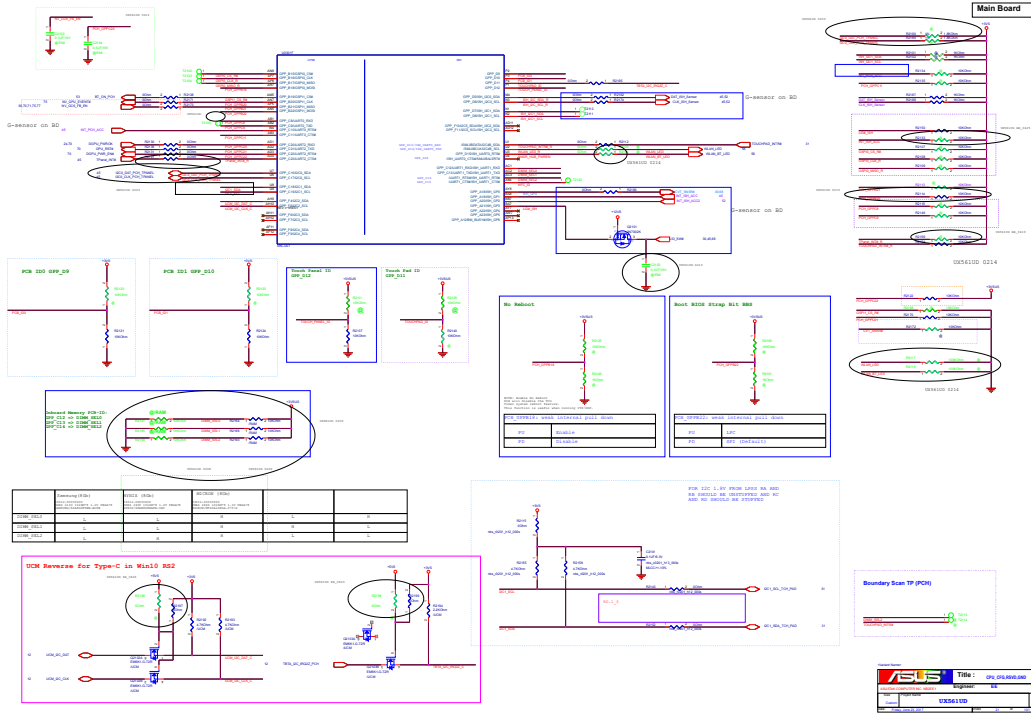
Figure 4-51. SKL U DDR4/-RS x8 Devices Memory Down V_{REF_CA} Overview

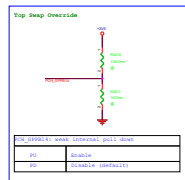
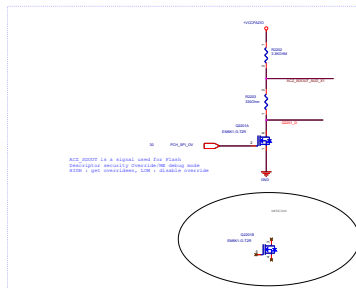
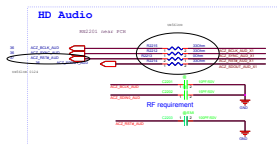
on-board Vref

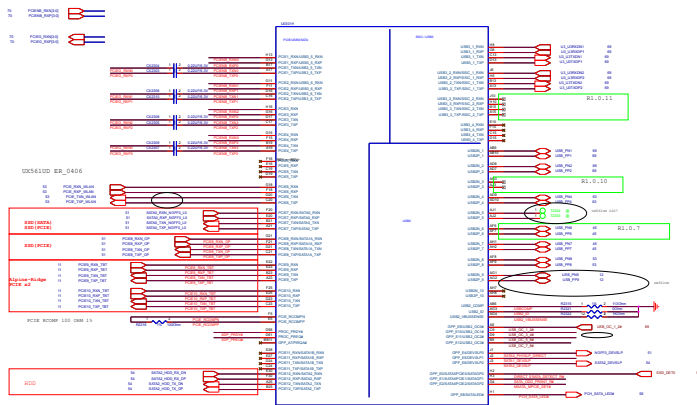
Figure 4-49. SKL U DDR4/-RS SODIMM V_{REF_CA} Overview

SO-DIMM Vref









USB 2.0		USB 3.0	
1	HP port (Charge)	1	USB 3.0 Port (Charge)
2	HP port	2	USB 3.0 Port
3	MC	3	MC
4	Card Reader	4	MC
5	MC	5	MC
6	M and VOLT Controller	6	MC
7	TSSOP FAN6102	7	MC
8	BT Alpine Ridge LP	8	MC
9	MC	9	MC



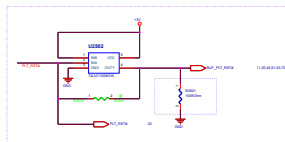
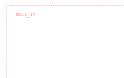
PORT 0-15	DATA USAGE	Co-Lay	Clock
PORT 0	DATA		Port 0
PORT 1	DATA		
PORT 2	DATA		
PORT 3	DATA		
PORT 4	DATA		
PORT 5	DATA		
PORT 6	DATA		
PORT 7	DATA		
PORT 8	DATA		
PORT 9	DATA		
PORT 10	DATA		
PORT 11	DATA		
PORT 12	DATA		
PORT 13	DATA		
PORT 14	DATA		
PORT 15	DATA		

DATA PORT	DATA USAGE	DEFAULT OPTION
PORT 0	DATA	
PORT 1	DATA	
PORT 2	DATA	

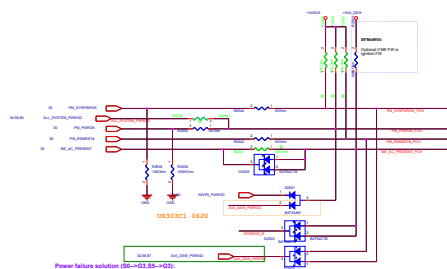
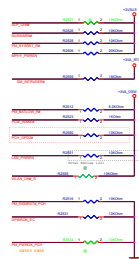


TS for Boundary Scan Test	
TS	TS
TS	TS

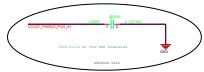
Main Board

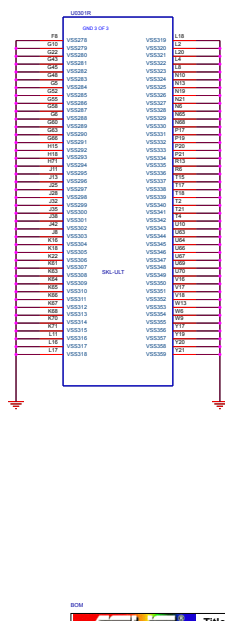
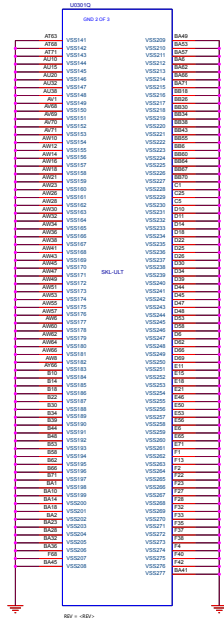
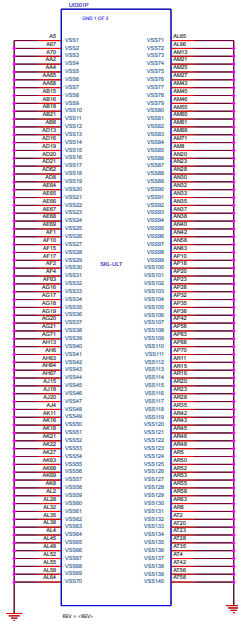


PEX_1_1



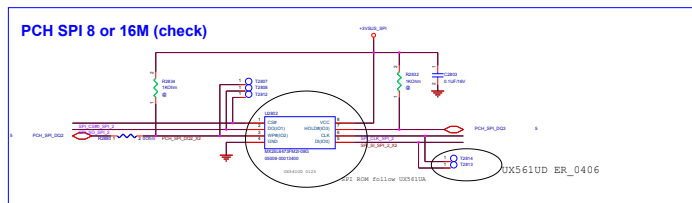
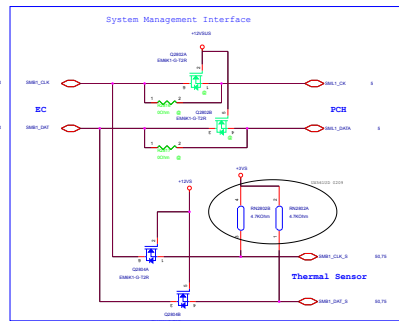
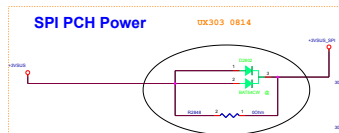
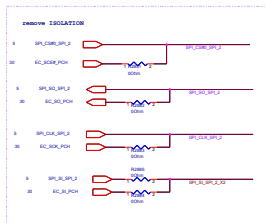
Power failure solution (D0→D3, D5→D3):





BOM

		Title : CPU_PCH_POEWR.GND	
ASUS COMPUTER INC. NED		Engineer: EE	
Size B	Project Name UX561UD	Rev R2.0	
Date Friday, June 23, 2017	Sheet 27	of 107	



(4499)
Ref: 0000-0013403
201

T2814 and T2813 need place at bot side

ASUS		Title : PCH-SPI ROM.0TH	
ASUS SYSTEMS CORPORATION		Engineer: EE	
Rev	Rev	Rev	Rev
40	UX561UD	Rev	Rev
Date: 2014-09-24 10:07		Rev: 00	

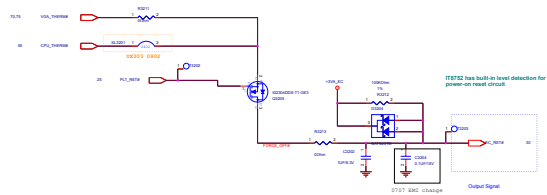
EC 8585

Only 3V Tolerance

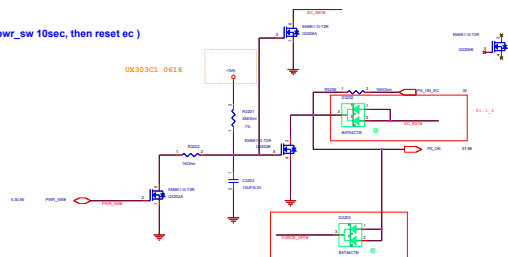
```

GPD[5,1,2,3,4,5,6]
GPD[7,8,9,10,11]
GPD[14,15,16,17]
GPD[18,19]
GPD[20,21]
GPD[22,23]
GPD[24,25]
GPD[26,27]
GPD[28,29]
GPD[30,31]
GPD[32,33]
GPD[34,35]
GPD[36,37]
GPD[38,39]
GPD[40,41]
GPD[42,43]
GPD[44,45]
GPD[46,47]
GPD[48,49]
GPD[50,51]
GPD[52,53]
GPD[54,55]
GPD[56,57]
GPD[58,59]
GPD[60,61]
GPD[62,63]
GPD[64,65]
GPD[66,67]
GPD[68,69]
GPD[70,71]
GPD[72,73]
GPD[74,75]
GPD[76,77]
GPD[78,79]
GPD[80,81]
GPD[82,83]
GPD[84,85]
GPD[86,87]
GPD[88,89]
GPD[90,91]
GPD[92,93]
GPD[94,95]
GPD[96,97]
GPD[98,99]
GPD[100,101]
GPD[102,103]
GPD[104,105]
GPD[106,107]
GPD[108,109]
GPD[110,111]
GPD[112,113]
GPD[114,115]
GPD[116,117]
GPD[118,119]
GPD[120,121]
GPD[122,123]
GPD[124,125]
GPD[126,127]
GPD[128,129]
GPD[130,131]
GPD[132,133]
GPD[134,135]
GPD[136,137]
GPD[138,139]
GPD[140,141]
GPD[142,143]
GPD[144,145]
GPD[146,147]
GPD[148,149]
GPD[150,151]
GPD[152,153]
GPD[154,155]
GPD[156,157]
GPD[158,159]
GPD[160,161]
GPD[162,163]
GPD[164,165]
GPD[166,167]
GPD[168,169]
GPD[170,171]
GPD[172,173]
GPD[174,175]
GPD[176,177]
GPD[178,179]
GPD[180,181]
GPD[182,183]
GPD[184,185]
GPD[186,187]
GPD[188,189]
GPD[190,191]
GPD[192,193]
GPD[194,195]
GPD[196,197]
GPD[198,199]
GPD[200,201]
GPD[202,203]
GPD[204,205]
GPD[206,207]
GPD[208,209]
GPD[210,211]
GPD[212,213]
GPD[214,215]
GPD[216,217]
GPD[218,219]
GPD[220,221]
GPD[222,223]
GPD[224,225]
GPD[226,227]
GPD[228,229]
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GPD[1834,1835]
GPD[1836,1837]
GPD[1838,1839]
GPD[1840,1841]
GPD[1842,1843]
GPD[1844,1845]
GPD[1846,1847]
GPD[1848,1849]
GPD[1850,1851]
GPD[1852,1853]
GPD[1854,1855]
GPD[1856,1857]
GPD[1858,1859]
GPD[1860,1861]
GPD[1862,1863]
GPD[1864,1865]
GPD[1866,1867]
GPD[1868,1869]
GPD[1870,1871]
GPD[1872,1873]
GPD[1874,1875]
GPD[1876,1877]
GPD[1878,1879]
GPD[1880,1881]
GPD[1882,1883]
GPD[1884,1885]
GPD[1886,1887]
GPD[1888,1889]
GPD[1890,1891]
GPD[1892,1893]
GPD[1894,1895]
GPD[1896,1897]
GPD[1898,1899]
GPD[1900,1901]
GPD[1902,1903]
GPD[1904,1905]
GPD[1906,1907]
GPD[1908,1909]
GPD[1910,1911]
GPD[1912,1913]
GPD[1914,1915]
GPD[1916,1917]
GPD[1918,1919]
GPD[1920,1921]
GPD[1922,19
```

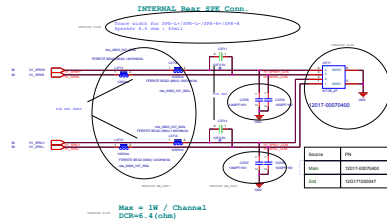
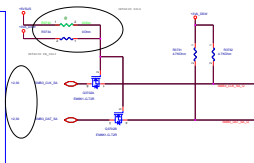
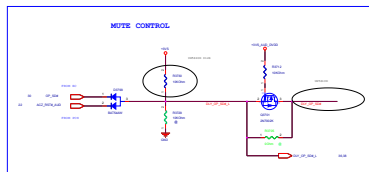

Thermal Policy



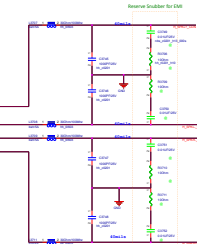
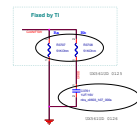
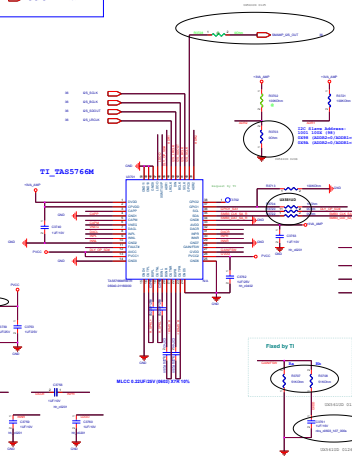
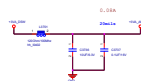
battery embedded (press pwr_sw 10sec, then reset ec)



NO. 1 17

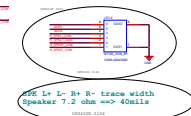


Source	Pin
Max	0.017500000000000000
Min	0.017500000000000000

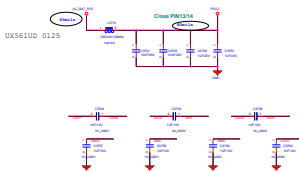


Max = 1W / Channel
DCR=7.2 (ohm)

INTERNAL Front SPK Conn.

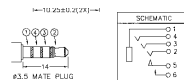


Max = 1.5W / Channel = 3W
Power Efficiency 85% -> 3.75 W
I=0.35 A@12V 0.2A@19V

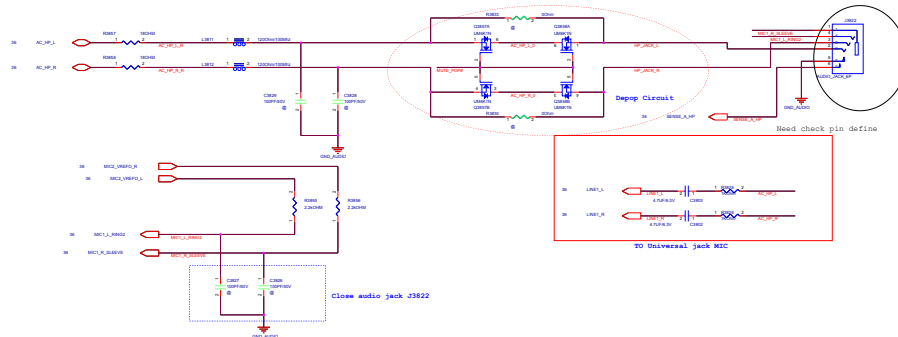


ASUS			
Title: ASUS AMP L SPK			
Pin:	0.017500000000000000	Regulate:	Max_Ping

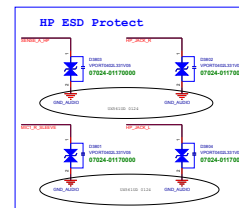
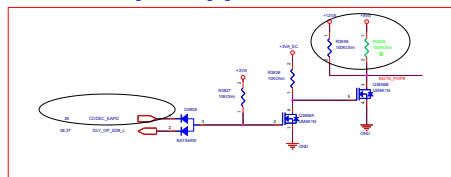
Universal Jack (Normal open type)



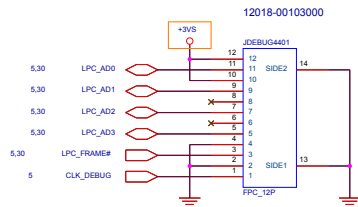
Global Header
Normal Open
Supported iPhone/Micin header, headphone



Headphone depop circuit



LPC Debug Port



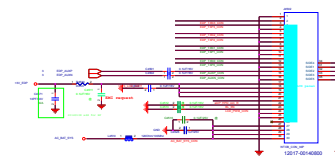
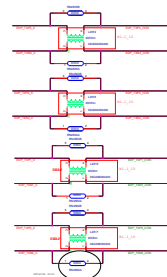
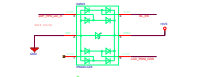
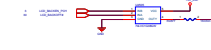
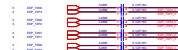
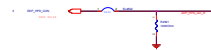
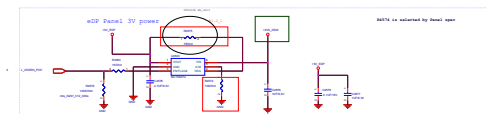
Q553UB SR

+3V-->+3VS

防止s3睡不下去的問題

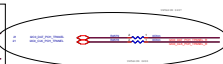
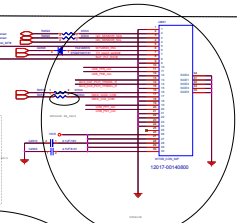
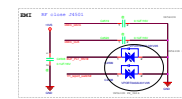
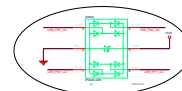
<Variant Name>

		Title : BUG_Debug	
ASUSTek COMPUTER INC. NB4		Engineer: EE	
Size	UX561UD		Rev
A4			R2.0
Date: Friday, June 23, 2017	Sheet	44	of 101

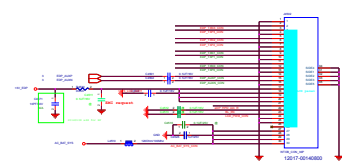
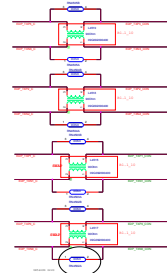
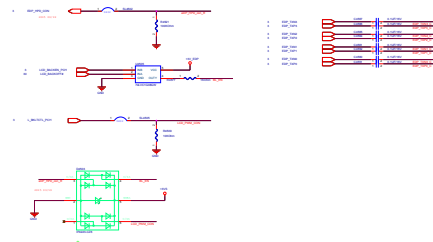
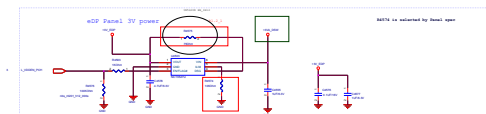


Device	Pin
U1	12017-02140800
U2	12017-02140800
U3	12017-02140800
U4	12017-02140800

Digital ALS

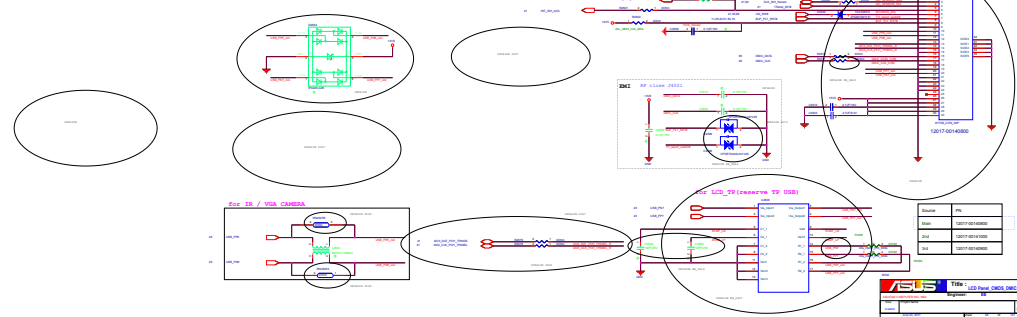


Device	Pin
U1	12017-02140800
U2	12017-02140800
U3	12017-02140800
U4	12017-02140800



Device	Pin
U1	12017-02140800
U2	12017-02140800
U3	12017-02140800
U4	12017-02140800

Digital ALS

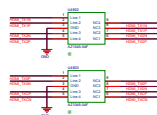
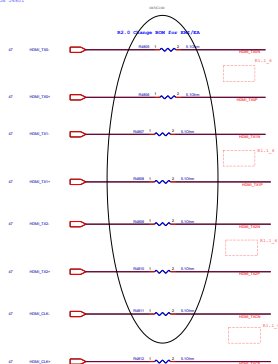


Main Board

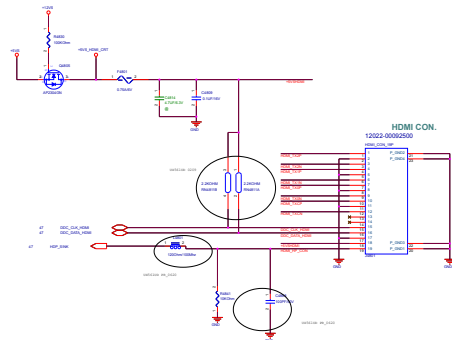


Close to CONNECTOR

HW41 CDR 24331

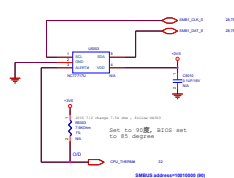


ch0000 int: 09002090330
Zwt: 09002090109

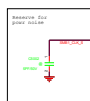


0X303 0918

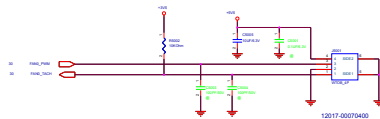
CPU Thermal Sensor



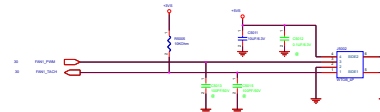
Temp.	Resolution
75	2500mV
80	7.5kOhm
100	15.5kOhm
120	2.5kOhm
110	10.7kOhm



CPU FAN

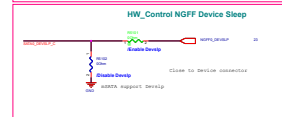
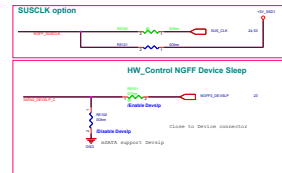
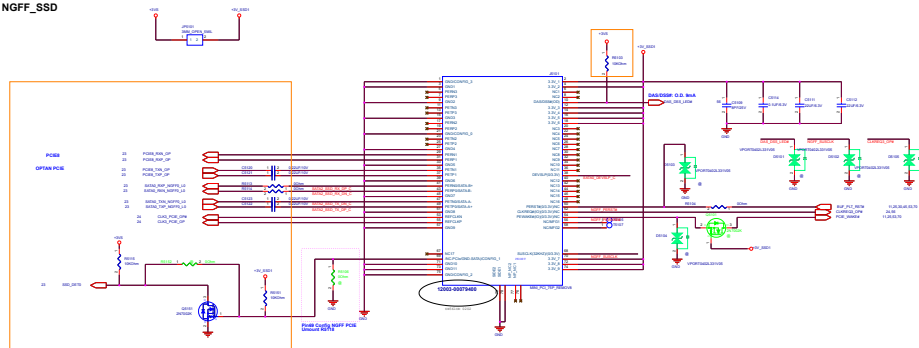


GPU FAN



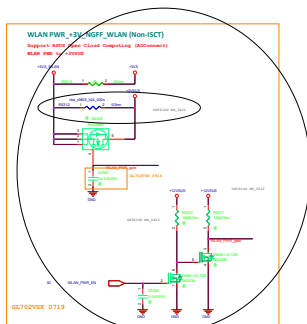
Source	PN
Main	12017-00070400
2nd	120170000007

NGFF_SSD

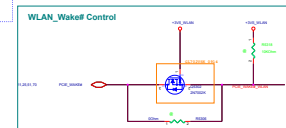
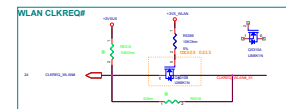
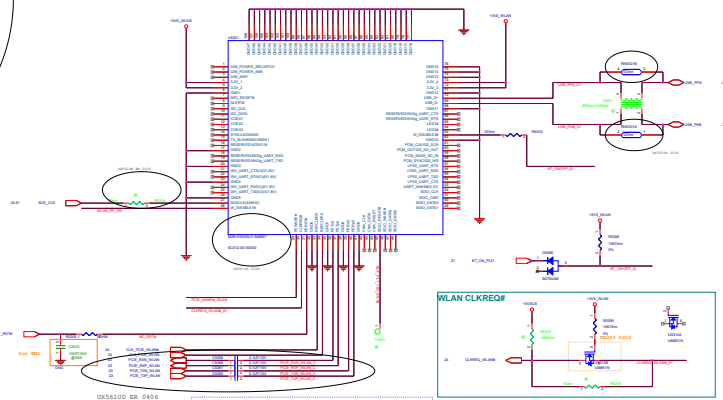
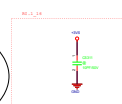
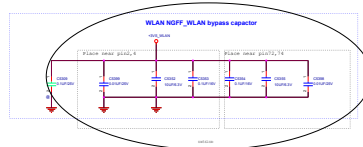


HW Disable SSD Develp	Mount R5102=0ohm, Dismount R5101=0ohm
HW Enable SSD Develp	Dismount R5102=0ohm, Mount R5101=0ohm

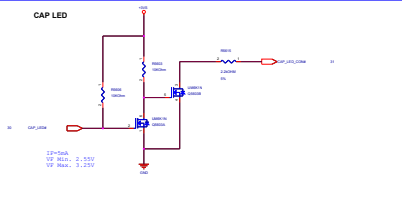
HW Enable SSD Develp	Unmount R5102=0ohm, Mount R5101=0ohm
----------------------	--------------------------------------



Check ERP lot 6 test and then NC at PR

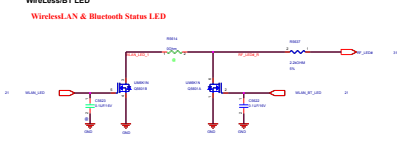


CAP LED

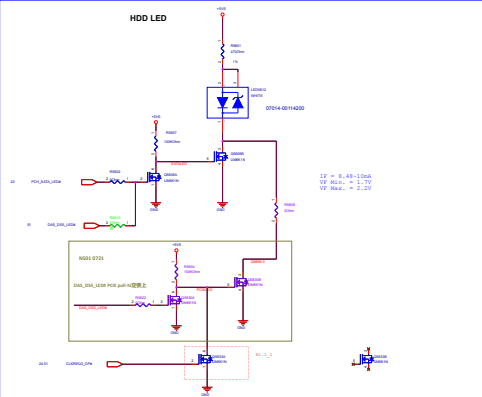


Wireless/BT LED

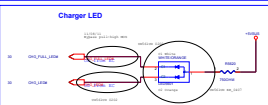
Wireless.LAN & Bluetooth Status LED



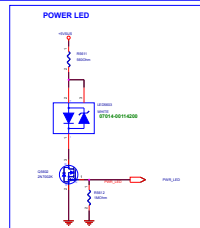
HDD LED



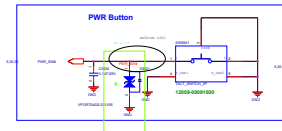
Charger LED



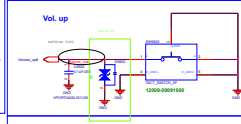
POWER LED



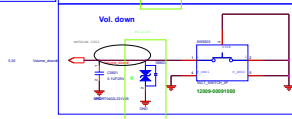
PWR Button



Vol. up



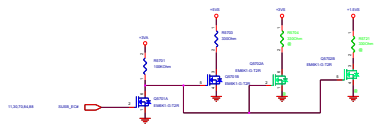
Vol. down



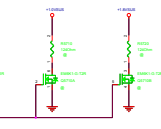
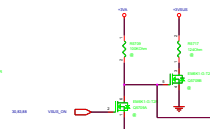
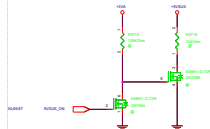
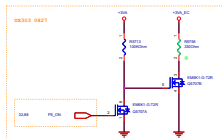
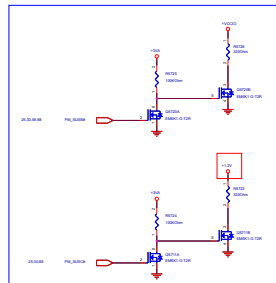
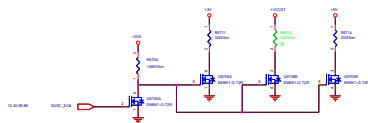
Source	Pin
Main	1702H-1701-1703
2nd	1701H-1701-1703

Reference

ASUS	Title :	LED Indicator
Product Code: 1701H-1703	EE	
1701H-1703	UX561SD	



4/20 Stuff R0710 and R0711

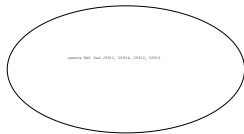
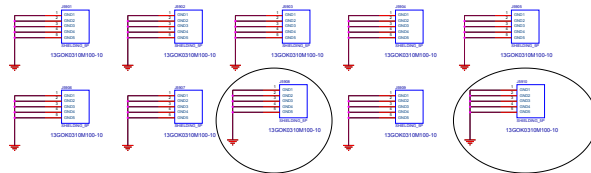


Main Board





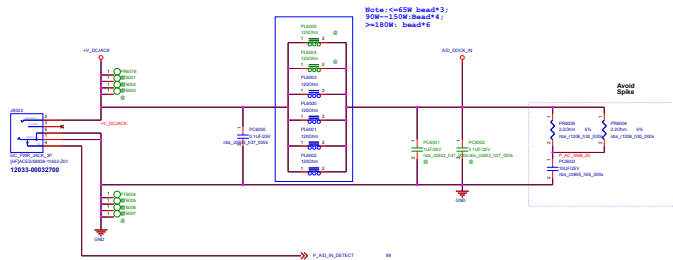
D3M01 CL1P



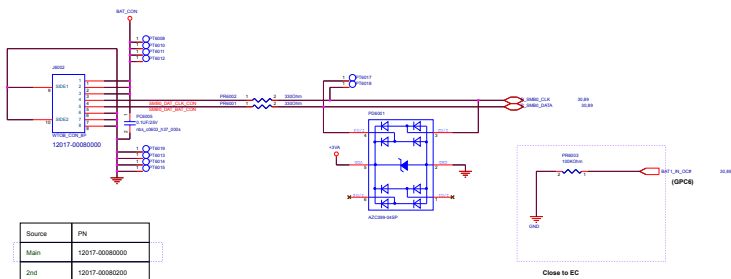
Source: 002, 003, 004, 005, 006, 007, 008, 009, 010, 011, 012, 013, 014, 015, 016, 017, 018, 019, 020, 021, 022, 023, 024, 025, 026, 027, 028, 029, 030, 031, 032, 033, 034, 035, 036, 037, 038, 039, 040, 041, 042, 043, 044, 045, 046, 047, 048, 049, 050, 051, 052, 053, 054, 055, 056, 057, 058, 059, 060, 061, 062, 063, 064, 065, 066, 067, 068, 069, 070, 071, 072, 073, 074, 075, 076, 077, 078, 079, 080, 081, 082, 083, 084, 085, 086, 087, 088, 089, 090, 091, 092, 093, 094, 095, 096, 097, 098, 099, 100, 101, 102, 103, 104, 105, 106, 107, 108, 109, 110, 111, 112, 113, 114, 115, 116, 117, 118, 119, 120, 121, 122, 123, 124, 125, 126, 127, 128, 129, 130, 131, 132, 133, 134, 135, 136, 137, 138, 139, 140, 141, 142, 143, 144, 145, 146, 147, 148, 149, 150, 151, 152, 153, 154, 155, 156, 157, 158, 159, 160, 161, 162, 163, 164, 165, 166, 167, 168, 169, 170, 171, 172, 173, 174, 175, 176, 177, 178, 179, 180, 181, 182, 183, 184, 185, 186, 187, 188, 189, 190, 191, 192, 193, 194, 195, 196, 197, 198, 199, 200, 201, 202, 203, 204, 205, 206, 207, 208, 209, 210, 211, 212, 213, 214, 215, 216, 217, 218, 219, 220, 221, 222, 223, 224, 225, 226, 227, 228, 229, 230, 231, 232, 233, 234, 235, 236, 237, 238, 239, 240, 241, 242, 243, 244, 245, 246, 247, 248, 249, 250, 251, 252, 253, 254, 255, 256, 257, 258, 259, 260, 261, 262, 263, 264, 265, 266, 267, 268, 269, 270, 271, 272, 273, 274, 275, 276, 277, 278, 279, 280, 281, 282, 283, 284, 285, 286, 287, 288, 289, 290, 291, 292, 293, 294, 295, 296, 297, 298, 299, 300, 301, 302, 303, 304, 305, 306, 307, 308, 309, 310, 311, 312, 313, 314, 315, 316, 317, 318, 319, 320, 321, 322, 323, 324, 325, 326, 327, 328, 329, 330, 331, 332, 333, 334, 335, 336, 337, 338, 339, 340, 341, 342, 343, 344, 345, 346, 347, 348, 349, 350, 351, 352, 353, 354, 355, 356, 357, 358, 359, 360, 361, 362, 363, 364, 365, 366, 367, 368, 369, 370, 371, 372, 373, 374, 375, 376, 377, 378, 379, 380, 381, 382, 383, 384, 385, 386, 387, 388, 389, 390, 391, 392, 393, 394, 395, 396, 397, 398, 399, 400, 401, 402, 403, 404, 405, 406, 407, 408, 409, 410, 411, 412, 413, 414, 415, 416, 417, 418, 419, 420, 421, 422, 423, 424, 425, 426, 427, 428, 429, 430, 431, 432, 433, 434, 435, 436, 437, 438, 439, 440, 441, 442, 443, 444, 445, 446, 447, 448, 449, 450, 451, 452, 453, 454, 455, 456, 457, 458, 459, 460, 461, 462, 463, 464, 465, 466, 467, 468, 469, 470, 471, 472, 473, 474, 475, 476, 477, 478, 479, 480, 481, 482, 483, 484, 485, 486, 487, 488, 489, 490, 491, 492, 493, 494, 495, 496, 497, 498, 499, 500, 501, 502, 503, 504, 505, 506, 507, 508, 509, 510, 511, 512, 513, 514, 515, 516, 517, 518, 519, 520, 521, 522, 523, 524, 525, 526, 527, 528, 529, 530, 531, 532, 533, 534, 535, 536, 537, 538, 539, 540, 541, 542, 543, 544, 545, 546, 547, 548, 549, 550, 551, 552, 553, 554, 555, 556, 557, 558, 559, 560, 561, 562, 563, 564, 565, 566, 567, 568, 569, 570, 571, 572, 573, 574, 575, 576, 577, 578, 579, 580, 581, 582, 583, 584, 585, 586, 587, 588, 589, 590, 591, 592, 593, 594, 595, 596, 597, 598, 599, 600, 601, 602, 603, 604, 605, 606, 607, 608, 609, 610, 611, 612, 613, 614, 615, 616, 617, 618, 619, 620, 621, 622, 623, 624, 625, 626, 627, 628, 629, 630, 631, 632, 633, 634, 635, 636, 637, 638, 639, 640, 641, 642, 643, 644, 645, 646, 647, 648, 649, 650, 651, 652, 653, 654, 655, 656, 657, 658, 659, 660, 661, 662, 663, 664, 665, 666, 667, 668, 669, 670, 671, 672, 673, 674, 675, 676, 677, 678, 679, 680, 681, 682, 683, 684, 685, 686, 687, 688, 689, 690, 691, 692, 693, 694, 695, 696, 697, 698, 699, 700, 701, 702, 703, 704, 705, 706, 707, 708, 709, 710, 711, 712, 713, 714, 715, 716, 717, 718, 719, 720, 721, 722, 723, 724, 725, 726, 727, 728, 729, 730, 731, 732, 733, 734, 735, 736, 737, 738, 739, 740, 741, 742, 743, 744, 745, 746, 747, 748, 749, 750, 751, 752, 753, 754, 755, 756, 757, 758, 759, 760, 761, 762, 763, 764, 765, 766, 767, 768, 769, 770, 771, 772, 773, 774, 775, 776, 777, 778, 779, 780, 781, 782, 783, 784, 785, 786, 787, 788, 789, 790, 791, 792, 793, 794, 795, 796, 797, 798, 799, 800, 801, 802, 803, 804, 805, 806, 807, 808, 809, 810, 811, 812, 813, 814, 815, 816, 817, 818, 819, 820, 821, 822, 823, 824, 825, 826, 827, 828, 829, 830, 831, 832, 833, 834, 835, 836, 837, 838, 839, 840, 841, 842, 843, 844, 845, 846, 847, 848, 849, 850, 851, 852, 853, 854, 855, 856, 857, 858, 859, 860, 861, 862, 863, 864, 865, 866, 867, 868, 869, 870, 871, 872, 873, 874, 875, 876, 877, 878, 879, 880, 881, 882, 883, 884, 885, 886, 887, 888, 889, 890, 891, 892, 893, 894, 895, 896, 897, 898, 899, 900, 901, 902, 903, 904, 905, 906, 907, 908, 909, 910, 911, 912, 913, 914, 915, 916, 917, 918, 919, 920, 921, 922, 923, 924, 925, 926, 927, 928, 929, 930, 931, 932, 933, 934, 935, 936, 937, 938, 939, 940, 941, 942, 943, 944, 945, 946, 947, 948, 949, 950, 951, 952, 953, 954, 955, 956, 957, 958, 959, 960, 961, 962, 963, 964, 965, 966, 967, 968, 969, 970, 971, 972, 973, 974, 975, 976, 977, 978, 979, 980, 981, 982, 983, 984, 985, 986, 987, 988, 989, 990, 991, 992, 993, 994, 995, 996, 997, 998, 999, 1000.

Client Name

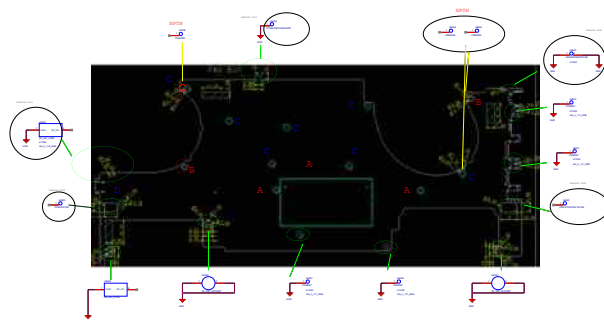
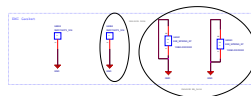
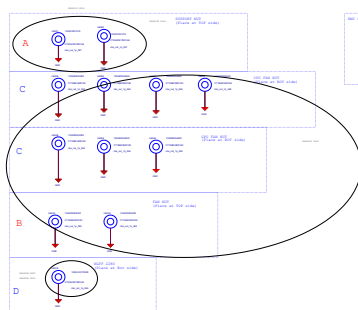
ASIS		Title : NGFF 0000	
Engineer: EE		Date: 01/01/2010	
Project: UX061UD		Rev: 01	
Drawn: 01/01/2010		Scale: 1:1	

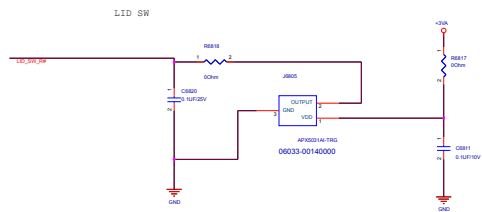


Battery Connector

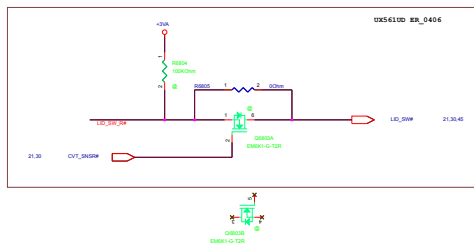


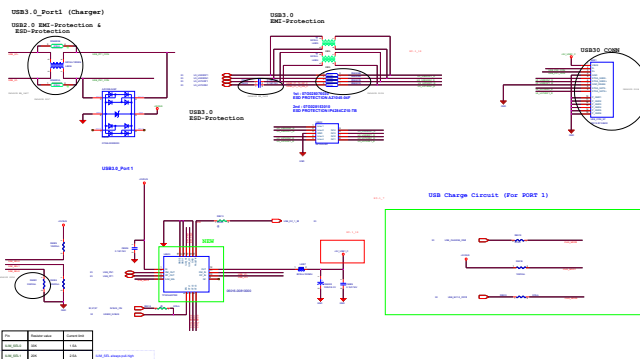
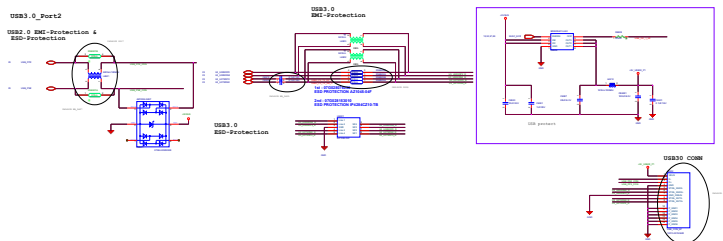
Source	PN
Main	12017-00080000
2nd	12017-00080200





Source	PN
Main	06033-00010000
2nd	06033-00140000

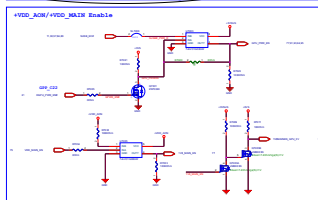
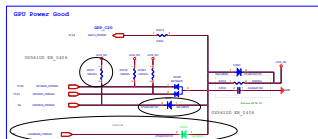
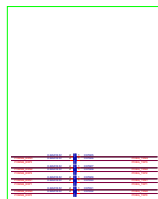




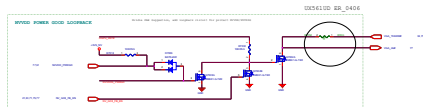
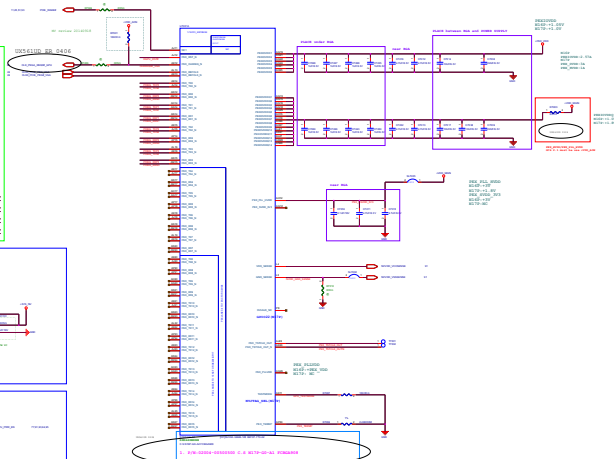
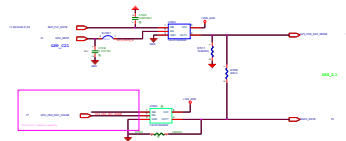
PCI EXPRESS

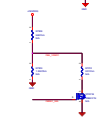


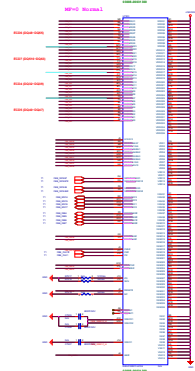
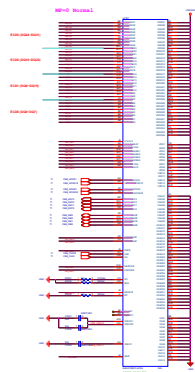
DESIGNED BY: [Name]
DATE: [Date]



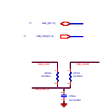
DGPD_PEX_RST



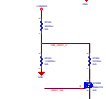
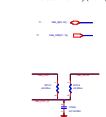




FBB Partition Memory (1 of 2)



FBB Partition Memory (2 of 2)



MF=0: Row=01 (00000000-0000000F)
MF=0: Row=02 (00000010-0000001F)
MF=0: Row=03 (00000020-0000002F)
MF=0: Row=04 (00000030-0000003F)
MF=0: Row=05 (00000040-0000004F)
MF=0: Row=06 (00000050-0000005F)
MF=0: Row=07 (00000060-0000006F)
MF=0: Row=08 (00000070-0000007F)
MF=0: Row=09 (00000080-0000008F)
MF=0: Row=0A (00000090-0000009F)
MF=0: Row=0B (000000A0-000000AF)
MF=0: Row=0C (000000B0-000000BF)
MF=0: Row=0D (000000C0-000000CF)
MF=0: Row=0E (000000D0-000000DF)
MF=0: Row=0F (000000E0-000000EF)
MF=0: Row=10 (000000F0-000000FF)

MF=0: Row=01 (00000000-0000000F)
MF=0: Row=02 (00000010-0000001F)
MF=0: Row=03 (00000020-0000002F)
MF=0: Row=04 (00000030-0000003F)
MF=0: Row=05 (00000040-0000004F)
MF=0: Row=06 (00000050-0000005F)
MF=0: Row=07 (00000060-0000006F)
MF=0: Row=08 (00000070-0000007F)
MF=0: Row=09 (00000080-0000008F)
MF=0: Row=0A (00000090-0000009F)
MF=0: Row=0B (000000A0-000000AF)
MF=0: Row=0C (000000B0-000000BF)
MF=0: Row=0D (000000C0-000000CF)
MF=0: Row=0E (000000D0-000000DF)
MF=0: Row=0F (000000E0-000000EF)
MF=0: Row=10 (000000F0-000000FF)

MF=0: Row=01	MF=0: Row=02	MF=0: Row=03	MF=0: Row=04	MF=0: Row=05	MF=0: Row=06	MF=0: Row=07	MF=0: Row=08	MF=0: Row=09	MF=0: Row=0A	MF=0: Row=0B	MF=0: Row=0C	MF=0: Row=0D	MF=0: Row=0E	MF=0: Row=0F	MF=0: Row=10
00000000	00000010	00000020	00000030	00000040	00000050	00000060	00000070	00000080	00000090	000000A0	000000B0	000000C0	000000D0	000000E0	000000F0

Diagram illustrating the 32-bit ARMv7-M instruction format (R-Format). The instruction is divided into several fields:

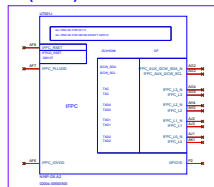
- OPCODE (4 bits):** Contains the instruction type (e.g., ADD, SUB, MOV).
- REGISTER (16 bits):** Contains the register number (Rn) and the shift amount (L).
- SHIFT (12 bits):** Contains the shift type (S) and the shift amount (L).

The diagram shows the instruction 0x00000000, which is a NOP instruction. The register field contains 0x00000000, and the shift field contains 0x00000000.

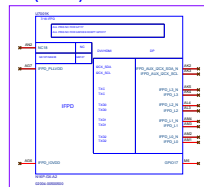
Figure 10-10 illustrates a 2D array of pointers to 2D arrays. The diagram shows a 2D array of pointers (PTRL_0000 to PTRL_0003) pointing to a 2D array of pointers (PTRL_0000 to PTRL_0003). The pointers are arranged in a grid, with each row of pointers pointing to a corresponding row of the 2D array. The pointers are labeled with their memory addresses and the values they point to.

[illegible]

DP(link C)



DVI(link D)

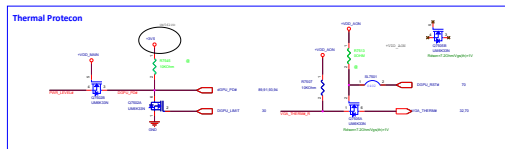
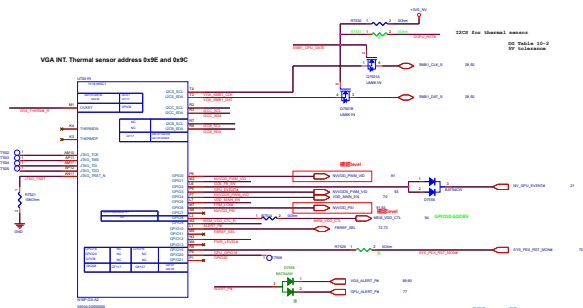
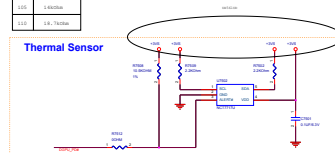


GPIO, TEMP SENSOR, JTAG

GPIO ASSIGNMENTS

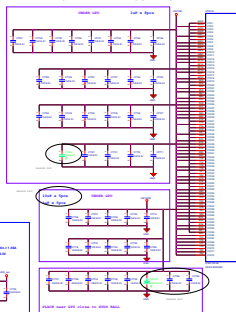
SPID	PID	ACTIVE	STATUS
0			SQLWALVE_PID
1	0007		SQL_PSN_PID
2	0007		SQL_AGENT_MANAGER
3	0007		SQL_PSN
4	0007		SQL_AGENT_SM
5	0007		SQL_LCK
6	1K		SQLWALVE_PSN
7	0007		LCK_SM_PID
8	0007		SQLWALVE_CTL
9	1K		SQLWALVE_AGENT
10	0007		SQLWALVE_CTL
11	0007		LCK_PID
12	1K		SQLWALVE
13	0007		LCK_AGENT
14	1K		SQL_PPSN
15	0007		SQL_PPSN
16	1K		SQL_PSN_AGENT_MANAGER
17	0007		SQL_PPSN
18	1K		SQLWALVE/STANDARD
19	0007		SQLWALVE_PSN
20	0007		SQLWALVE
21	1K	SQLWALVE	SQLWALVE

Temp.	Resistor
75	2kOhm
90	7.5kOhm
100	10.5kOhm
105	14kOhm
110	18.7kOhm

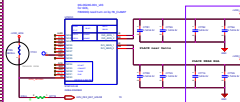
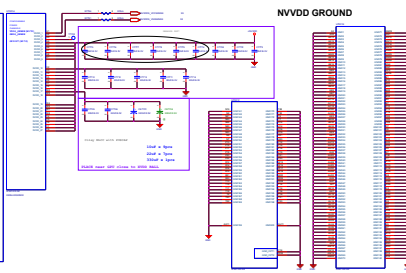


NVDD Power and Decoupling				
Pin	Power	Ground	Power	Ground
1	VDD	GND	1	GND
2	VDD	GND	2	GND
3	VDD	GND	3	GND
4	VDD	GND	4	GND
5	VDD	GND	5	GND

NVDD POWER AND DECOUPLING

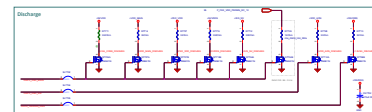
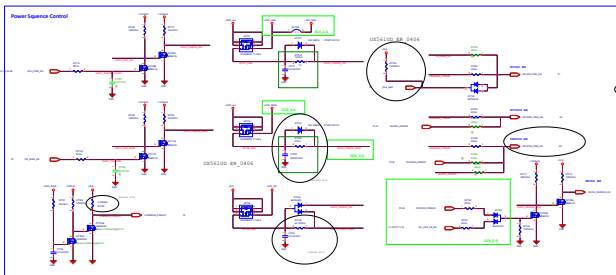


NVDD GROUND



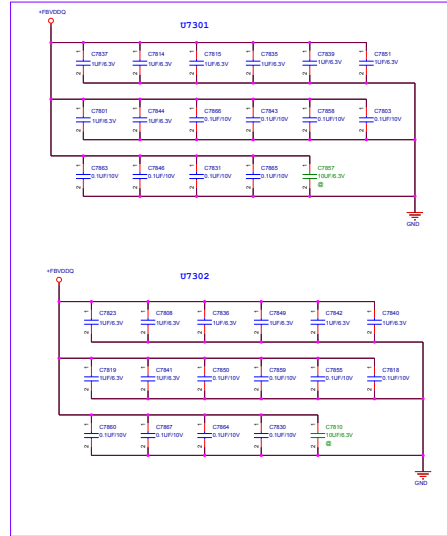
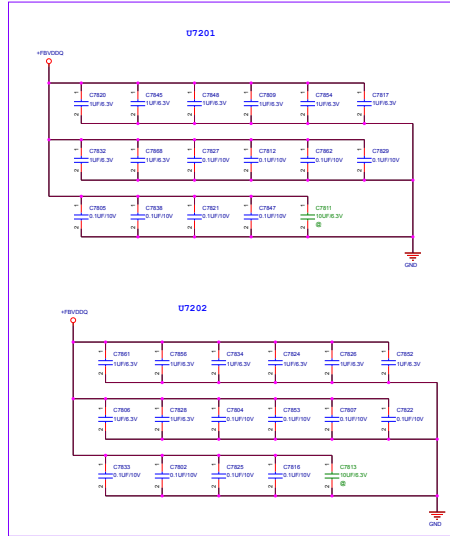
Pin	Power	Ground	Power	Ground
1	VDD	GND	1	GND
2	VDD	GND	2	GND
3	VDD	GND	3	GND
4	VDD	GND	4	GND
5	VDD	GND	5	GND

Pin	Power	Ground	Power	Ground
1	VDD	GND	1	GND
2	VDD	GND	2	GND
3	VDD	GND	3	GND
4	VDD	GND	4	GND
5	VDD	GND	5	GND

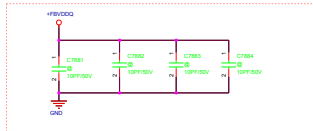


Location is close to DRAM for clamshell mode

0.1uF X8
1uF X 8

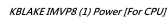


R0.1_16



Variant Name

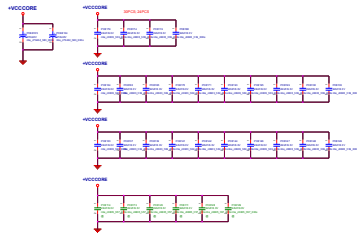
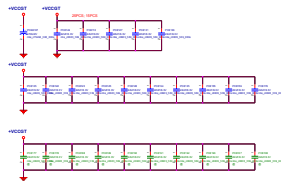
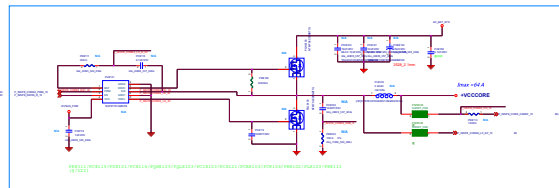
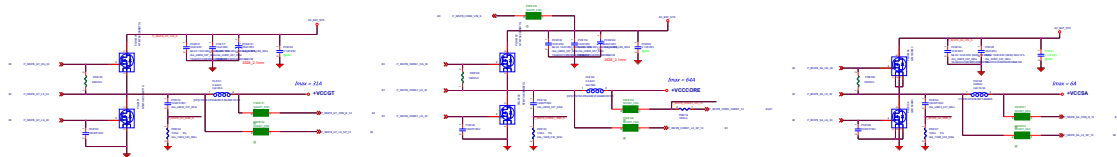
ASUS		Title : VRAM CAP	
ASUSTeK COMPUTER INC. (AS)		Engineer: Mario_jhu	
Size		Rev	
B		UX561UD	R2.0
Date: Friday, June 23, 2017		Drawn	18 of 101



U42/U22 BOM optional

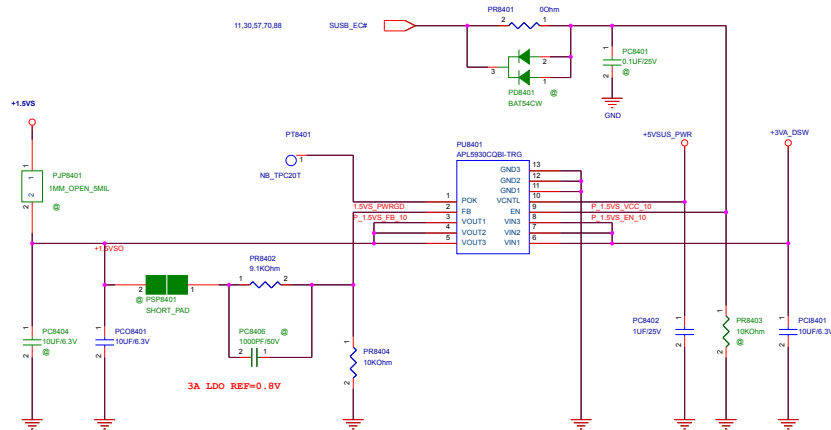
Project Name		U3561UD	
Title:		SPE, FLMRY, SAS, VIZ, INTPP	
Dept:	ASUS Power Team	Engineer:	Power

Kaby Lake-U IMVP8 Power (2) [For CPU]



Toggle FullScreen

+1.5VS [For Audio]



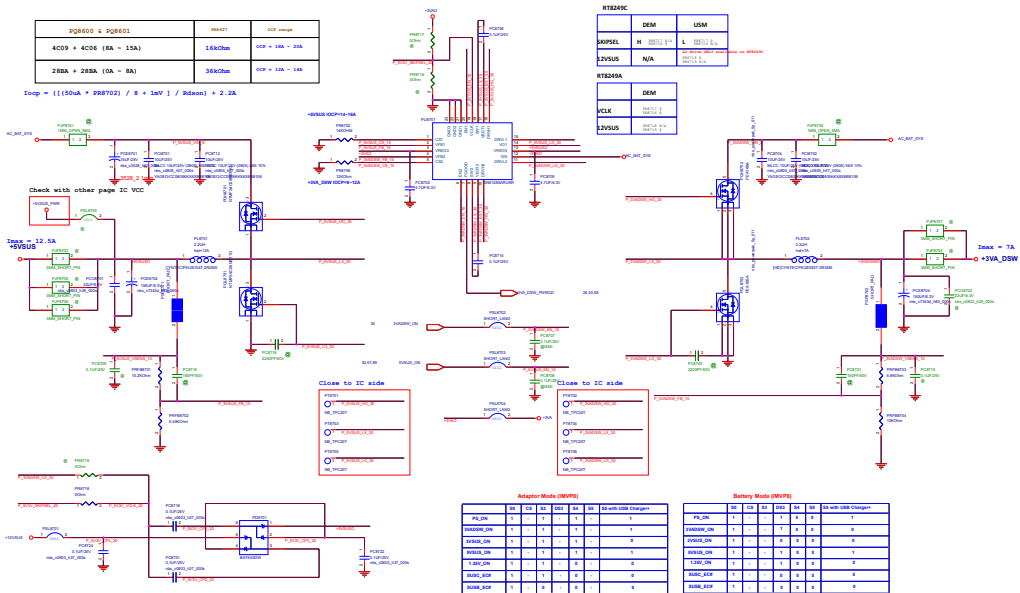
<Variant Name>

		Project Name	Rev
		UX561UD	R2.0
Title : +1.5VS			
Size	Dept.:	ASUSTek COMPUTER INC.	Engineer: Rock
Custom			
Date: Friday, June 23, 2017	Sheet	84	of 102

+3VA_DSW / +5VSUS [System Power]

PQ600 & PQ601	WB227	OCF range
4C09 + 4C06 (BA ~ 15A)	16kOhm	OCF = 18A - 20A
20BA + 20BA (0A ~ 8A)	36kOhm	OCF = 12A - 14A

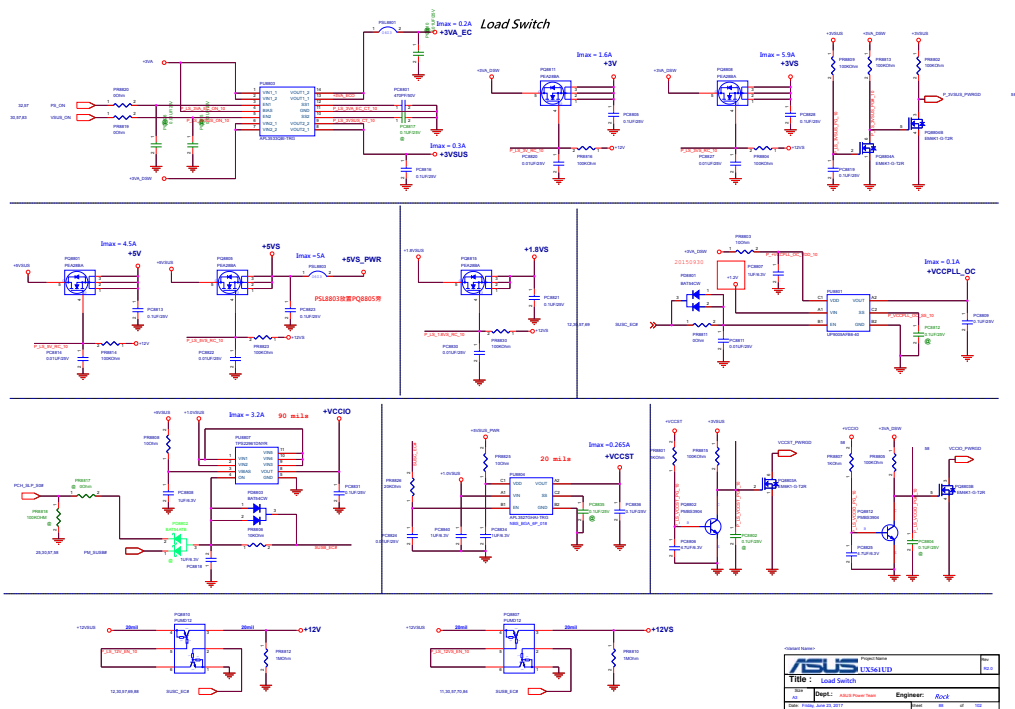
$$I_{ocp} = \{ [(50\mu A * PR8702) / 8 + 1mV] / R_{dson} \} + 2.2A$$

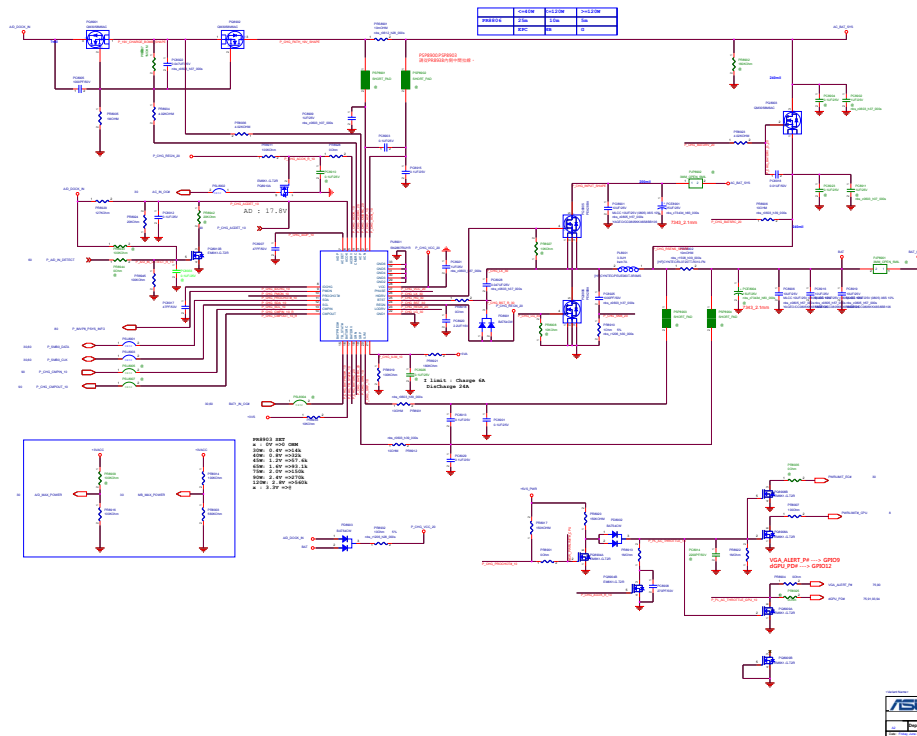


請 check 零件規格 +12V3.0A total 等資料以免誤不誤小於10k Ohm

Adaptor Mode (MVP)					
	SS	CS	DS	SS	SS with USB Charger
PS_ON	1	-	1	-	1
EVMSG_ON	1	-	1	-	1
EVMSG_ON	1	-	1	-	0
EVMSG_ON	1	-	1	-	1
EVMSG_ON	1	-	1	-	0
EVMSG_ON	1	-	1	-	0
EVMSG_ON	1	-	1	-	0

Battery Mode (MPPE)						
	SE	CS	SS	SS2	SS	SS with USB Charger
PS_ON	1	-	-	1	0	1
PMODE_ON	1	-	-	1	0	0
SVPS_ON	1	-	-	0	0	0
SVPS2_ON	1	-	-	1	0	1
1.2V_ON	1	-	-	1	0	0
SUSC_SCP	1	-	-	0	0	0
SUSC_XCP	1	-	-	0	0	0

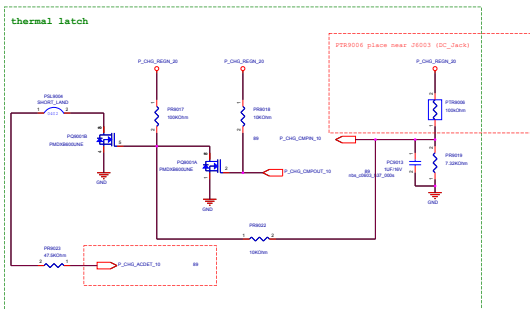
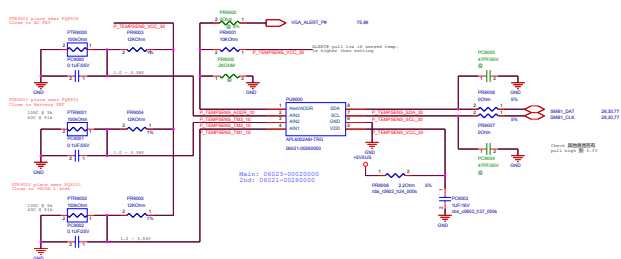




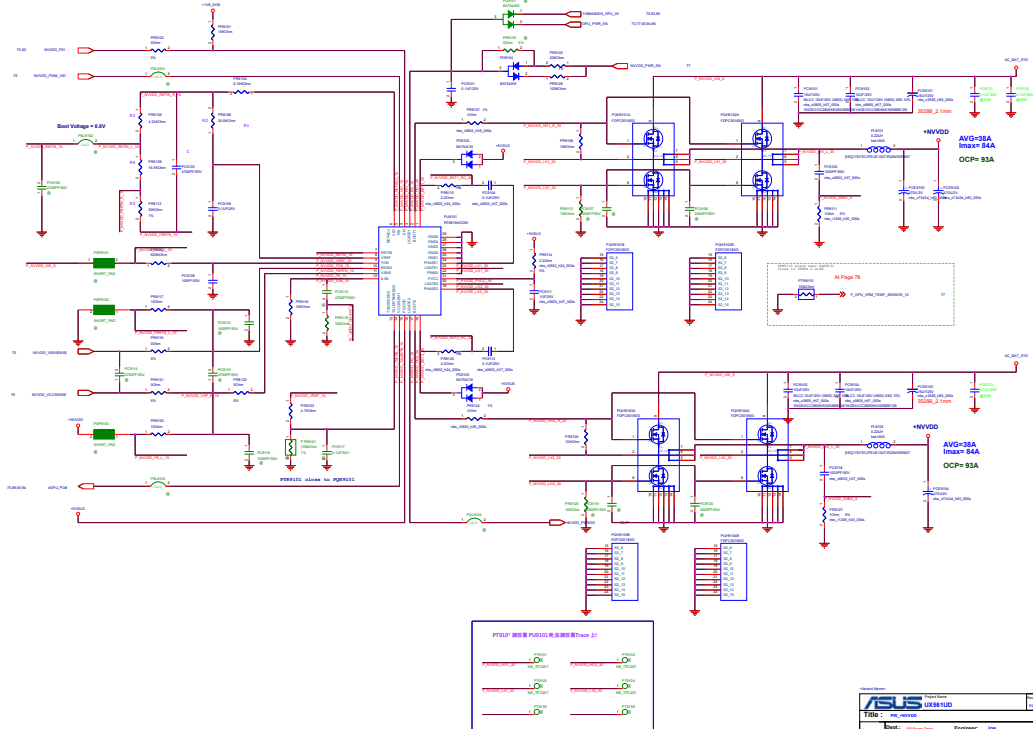
Address Selection Table

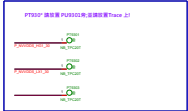
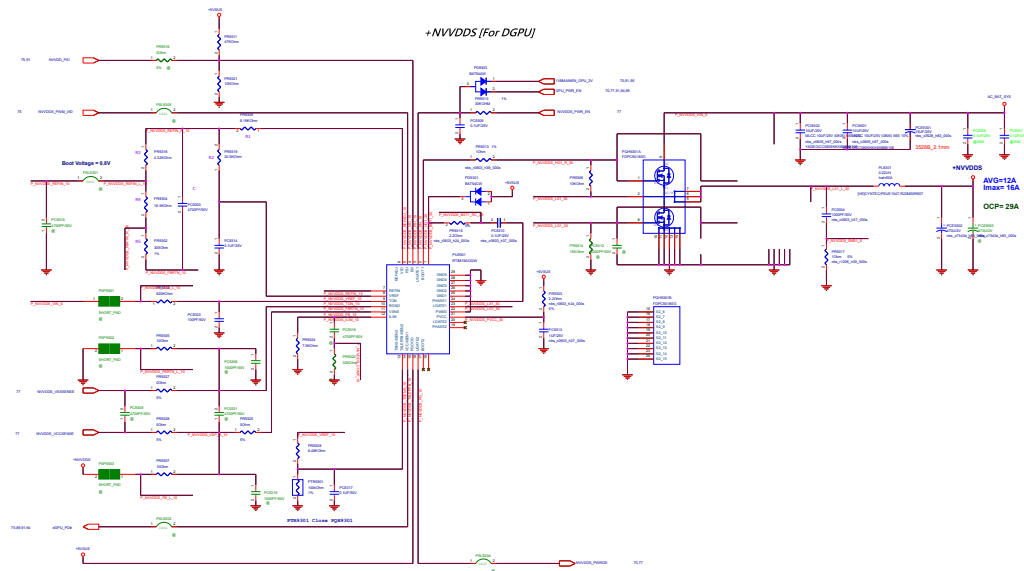
Address	OpTE	OpTC	OpTB	OpTH	OpTE	OpTB	OpT2	OpT3
PR001	10b	1.5b	2b	3.5b	3.5b	6.3b	5.1b	6b
PR002	Opere	8.7b	8.7b	8.8b	8.7b	8.4b	7.7b	7b

Address	3aD0	3aD1	3aD2	3aD3	3aD4	3aD5	3aD6
R/W	W	W	W	R	R	R	R
Function	Temp. alert threshold setting			Demand temp. data			bit 4 = 1 bit 5 = 2 bit 6 = 2 When ALERT? event



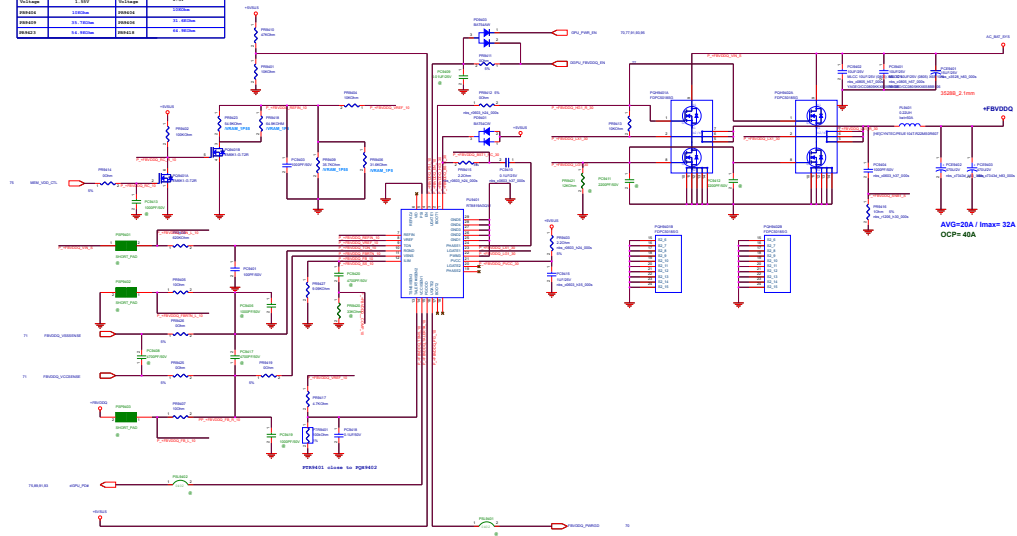
+NVVDD [For DGPU]





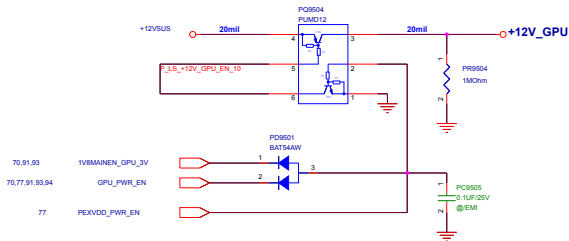
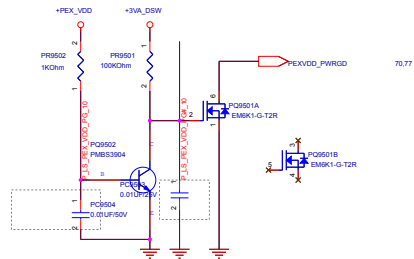
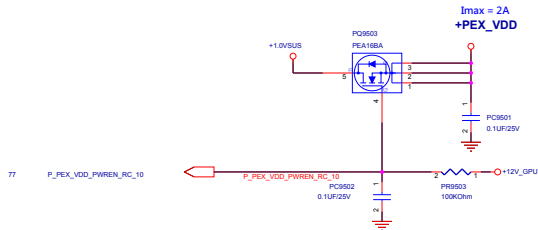
DVS Setting			
M1TF 40V (M1TF 40 M1TF 40 M1TF 40)		M1TF 40V (M1TF 40 M1TF 40 M1TF 40)	
Voltage	1.00V	Voltage	1.00V
000004	100000	000004	100000
000004	100000	000004	100000
000004	100000	000004	100000

+FBVDDQ (For VRAM)



PT5407 输出端 PU5401 输出端 Trace 21



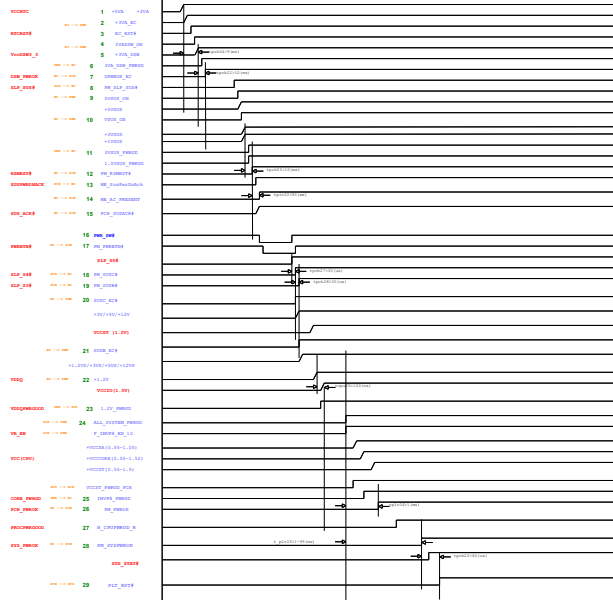


<Variant Name>

ASUS		Project Name	Rev
UX561UD			R2.0
Title : POWER_VGFX_CORE			
Size	Dept.: ASUS/tek COMPUTER INC	Engineer: Kevin	
A4			
Date: Friday, June 23, 2017	Sheet	95	of 101



AC-IN Mode



Power On Sequence Diagram Rev.R2.0

